

ABSTRACT OF THE DISCLOSURE

In one embodiment, the invention is directed to a data selection circuit for a general purpose performance counter ("GPPC") connected to a bus carrying debug data. The data selection circuit is connected to receive the debug data and comprises logic for receiving the debug data as a plurality of N -bit portions of block-aligned data and outputting a designated one of the N -bit portions; and circuitry for providing to the receiving logic a control signal for designating one of the N -bit portions.